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Jameco Part Number 800235

## FEATURES AND SPECIFICATIONS

### Features and Benefits

- Ultra low profile
- Enhanced panel grounding tabs on shielding RJ-45 configuration
- Enclosed top
- Surface Mount Compatible materials
- Pin through paste solderability
- 100% tested for hi-pot and continuity

### Reference Information

Product Specification: PSX-43202

Packaging: Tray

UL File No.: E107635

CSA File No.: LR19980

Use with: FCC 68 Plugs

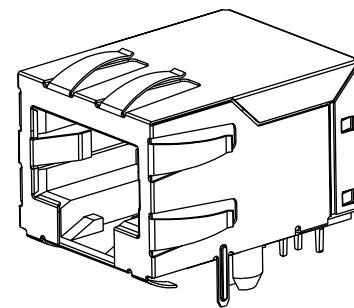
Designed in: Inches



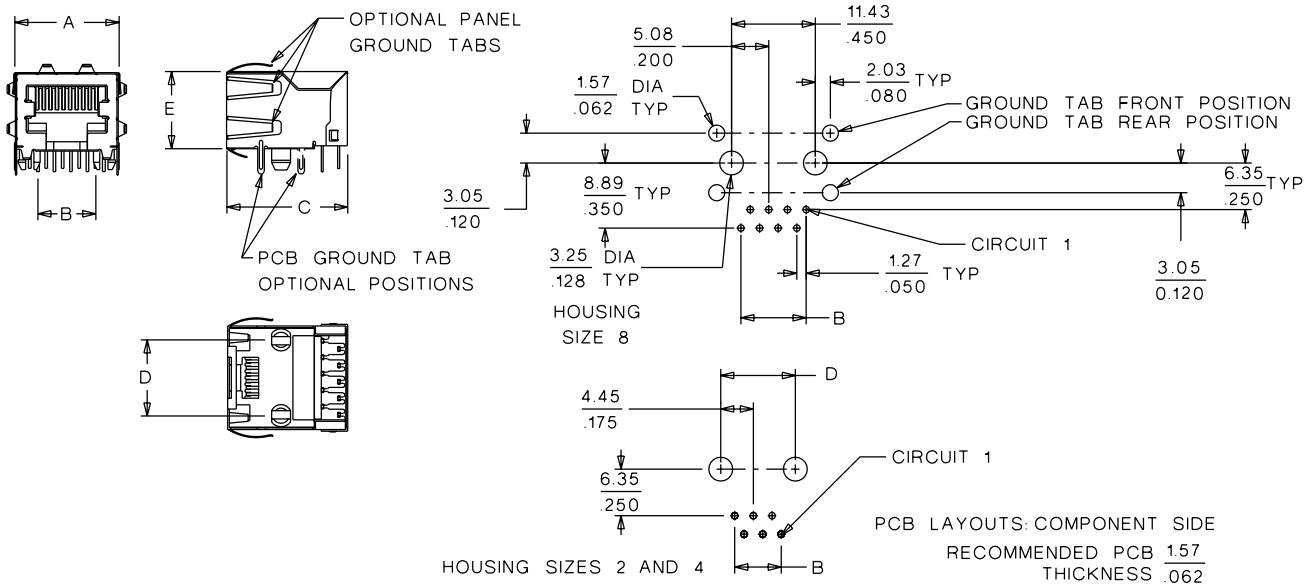
## Modular Jack

**43202**

### Right Angle, Low Profile Shielded and Unshielded Versions



## CATALOG DRAWING (FOR REFERENCE ONLY)



## ORDERING INFORMATION AND DIMENSIONS

Circuits	Loaded Contacts	Shield Panel Ground Tab Option	Order No.		Dimension				
			Unshielded	Shielded	A	B	C	D	E
4	2	43202-4104		11.18 (.440)	1.27 (.050)	18.03 (.710)	7.62 (.300)	11.58 (.456)	
	4	43202-4101		11.18 (.440)	3.81 (.150)	18.03 (.710)	7.62 (.300)	11.58 (.456)	
6	2	43202-6107		13.21 (.520)	1.27 (.050)	18.03 (.710)	10.16 (.400)	11.58 (.456)	
	4	43202-6104		13.21 (.520)	3.81 (.150)	18.03 (.710)	10.16 (.400)	11.58 (.456)	
8	6	43202-6101		13.21 (.520)	6.35 (.250)	18.03 (.710)	10.16 (.400)	11.58 (.456)	
	8	43202-8104		15.24 (.600)	8.89 (.350)	18.03 (.710)	11.43 (.450)	11.58 (.456)	
8	8	All Panel Ground Tabs	43202-8919	43202-8927	15.85 (.624)	8.89 (.350)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	8	Offset Panel Ground Tabs	43202-8918	43202-8926	15.85 (.624)	8.89 (.350)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	8	Top Panel Ground Tabs	43202-8917	43202-8925	15.85 (.624)	8.89 (.350)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	8	No Panel Ground Tabs	43202-8916	43202-8924	15.85 (.624)	8.89 (.350)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	10		43202-8101		15.24 (.600)	11.43 (.450)	18.03 (.710)	11.43 (.450)	11.58 (.456)
	10	All Panel Ground Tabs	43202-8903	43202-8911	15.85 (.624)	11.43 (.450)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	10	Offset Panel Ground Tabs	43202-8902	43202-8910	15.85 (.624)	11.43 (.450)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	10	Top Panel Ground Tabs	43202-8901	43202-8909	15.85 (.624)	11.43 (.450)	18.39 (.724)	11.43 (.450)	12.09 (.476)
	10	No Panel Ground Tabs	43202-8900	43202-8908	15.85 (.624)	11.43 (.450)	18.39 (.724)	11.43 (.450)	12.09 (.476)



# PRODUCT SPECIFICATION

## ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS

### 1.0 SCOPE

This Product Specification covers the 1.27 mm (.050 inch) centerline (pitch) printed circuit board (PCB) modular jack connector series with selective gold and tin-lead plating.

### 2.0 PRODUCT DESCRIPTION

#### 2.1 PRODUCT NAME AND SERIES NUMBER(S)

Ultra Low Profile Right Angle Modular Jacks 43202, 44796

#### 2.2 DIMENSIONS, MATERIALS, PLATINGS AND MARKINGS

See the appropriate sales drawings (SDA-43202, SDA-44796-001) for information on dimensions, materials, plating and markings.

#### 2.3 SAFETY AGENCY APPROVALS

UL File Number.....E107635

CSA File Number.....LR19980

### 3.0 APPLICABLE DOCUMENTS AND SPECIFICATIONS

FCC Rules and Regulations, Part 68, Subpart F

REA Bulletin 345-81, PE-76; Specification for modular telephone set hardware

ANSI/EIA/TIA-568

IEC-60603-7

UL 1863

MIL-STD-202; General requirements for test specifications

### 4.0 RATINGS

#### 4.1 VOLTAGE

56.5 V DC

150 V <sub>RMS</sub> AC (Ringing voltage only)

#### 4.2 CURRENT

1.5 Amps @ 25°C

#### 4.3 TEMPERATURE

Operating: - 40°C to + 70°C

REVISION: <b>C</b>	ECR/ECN INFORMATION: EC No: <b>UCR2004-0250</b> DATE: <b>2003/08/01</b>	TITLE: <b>PRODUCT SPECIFICATION ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS</b>	SHEET No. <b>1 of 5</b>
DOCUMENT NUMBER: <b>PS-43202-001</b>	CREATED / REVISED BY: <b>MKAMAR 01/09/19</b>	CHECKED BY: <b>MKAMAR 01/09/19</b>	APPROVED BY: <b>BWIRKUS 01/09/19</b>



# PRODUCT SPECIFICATION

## 5.0 PERFORMANCE

### 5.1 ELECTRICAL REQUIREMENTS

DESCRIPTION	TEST CONDITION	REQUIREMENT
<b>Contact Resistance (Low Level)</b>	Mate connectors: apply a maximum voltage of <b>20</b> mV and a current of <b>15</b> mA. (Measurement locations in Section 7.0)	<b>10</b> milliohms MAXIMUM [initial]
<b>Insulation Resistance</b>	Unmated connector, mounted to a PCB: apply a voltage of <b>500</b> VDC between adjacent terminals and between terminals to ground.	<b>500</b> Megohms MINIMUM
<b>Dielectric Withstanding Voltage</b>	Mate connectors: apply a voltage of <b>1000</b> VAC for <b>1</b> minute between adjacent terminals and between terminals to ground.	No breakdown; current leakage < <b>5</b> mA
<b>Temperature Rise</b>	Mate connectors: measure the temperature rise at the rated current after: <b>96</b> hours	Temperature rise; <b>+30°C</b> MAXIMUM

<u>REVISION:</u> <b>C</b>	<u>ECR/ECN INFORMATION:</u> <u>EC No:</u> UCR2004-0250 <u>DATE:</u> 2003/08/01	<u>TITLE:</u> <b>PRODUCT SPECIFICATION ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS</b>	<u>SHEET No.</u> <b>2 of 5</b>
<u>DOCUMENT NUMBER:</u> <b>PS-43202-001</b>	<u>CREATED / REVISED BY:</u> <b>MKAMAR 01/09/19</b>	<u>CHECKED BY:</u> <b>MKAMAR 01/09/19</b>	<u>APPROVED BY:</u> <b>BWIRKUS 01/09/19</b>
<i>TEMPLATE FILENAME: PRODUCT_SPEC(SIZE_A)(V.1).DOC</i>			



# PRODUCT SPECIFICATION

## 5.2 MECHANICAL REQUIREMENTS

	DESCRIPTION	TEST CONDITION	REQUIREMENT
	<b>Connector Mate Force</b>	Mate connector at a rate of <b>25 ± 6 mm (1 ± 1/4 inch)</b> per minute. (Gage dimensions in Section 7.0)	<b>22 N (5 lbf)</b> MAXIMUM insertion force
	<b>Durability</b>	Mate connectors up to <b>500</b> cycles at a maximum rate of <b>20</b> cycles per minute prior to Environmental Tests.	<b>10</b> milliohms MAXIMUM (change from initial)
	<b>Vibration (Random)</b>	Mate connectors and vibrate per MIL-STD-202	<b>10</b> milliohms MAXIMUM (change from initial) & Discontinuity < 1 microsecond
	<b>Plug Retention Force</b>	Apply an axial pullout force on the plug at a rate of <b>25 ± 6 mm (1 ± 1/4 inch)</b> .	<b>89 N (20 lbf)</b> MINIMUM retention force
	<b>PCB Separation Forces</b>	Apply a perpendicular static load on the plug at a rate of <b>25 ± 6 mm (1 ± 1/4 inch)</b> .	<b>4.5 N (1 lbf)</b> MINIMUM withdrawal force before solder reflow <b>89 N (20 lbf)</b> MINIMUM withdrawal force after solder reflow

<u>REVISION:</u> <b>C</b>	<u>ECR/ECN INFORMATION:</u> <u>EC No:</u> UCR2004-0250 <u>DATE:</u> 2003/08/01	<u>TITLE:</u> <b>PRODUCT SPECIFICATION ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS</b>	<u>SHEET No.</u> <b>3 of 5</b>
<u>DOCUMENT NUMBER:</u> <b>PS-43202-001</b>	<u>CREATED / REVISED BY:</u> <b>MKAMAR 01/09/19</b>	<u>CHECKED BY:</u> <b>MKAMAR 01/09/19</b>	<u>APPROVED BY:</u> <b>BWIRKUS 01/09/19</b>
<i>TEMPLATE FILENAME: PRODUCT_SPEC(SIZE_A)(V.1).DOC</i>			



# PRODUCT SPECIFICATION

## 5.3 ENVIRONMENTAL REQUIREMENTS

DESCRIPTION	TEST CONDITION	REQUIREMENT												
<b>Thermal (Cycling)</b>	Connectors to be placed in 95% relative humidity. Maximum temperature change is 15°C/hour. Cycle linearly per chart below. Mate connectors; expose to <b>10</b> cycles of: <table><thead><tr><th>Temperature °C</th><th>Duration (Minutes)</th></tr></thead><tbody><tr><td>30 to 5</td><td>120</td></tr><tr><td>5 to 30</td><td>120</td></tr><tr><td>Hold at 30</td><td>240</td></tr><tr><td>30 to 5</td><td>180</td></tr><tr><td>Hold at 5</td><td>180</td></tr></tbody></table>	Temperature °C	Duration (Minutes)	30 to 5	120	5 to 30	120	Hold at 30	240	30 to 5	180	Hold at 5	180	<b>10</b> milliohms MAXIMUM (change from initial) & Dielectric Withstanding Voltage: No Breakdown at 500 VAC & Insulation Resistance: 500 Megohms MINIMUM & Visual: No Damage
Temperature °C	Duration (Minutes)													
30 to 5	120													
5 to 30	120													
Hold at 30	240													
30 to 5	180													
Hold at 5	180													
<b>Solderability</b>	Dip solder tails in flux and immerse in solder bath at $230 \pm 5^\circ\text{C}$ for $3 \pm 0.5$ seconds.	Solder Wetting Visual: 95% of immersed area must show no voids, pin holes												
<b>Resistance to Soldering Heat</b>	Dip solder tails in molten solder and immerse in solder bath at $260 \pm 5^\circ\text{C}$ for $5 \pm 0.5$ seconds.	Visual: No Damage												

<u>REVISION:</u> <b>C</b>	<u>ECR/ECN INFORMATION:</u> <u>EC No:</u> UCR2004-0250 <u>DATE:</u> 2003/08/01	<u>TITLE:</u> <b>PRODUCT SPECIFICATION ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS</b>	<u>SHEET No.</u> <b>4 of 5</b>
<u>DOCUMENT NUMBER:</u> <b>PS-43202-001</b>	<u>CREATED / REVISED BY:</u> <b>MKAMAR 01/09/19</b>	<u>CHECKED BY:</u> <b>MKAMAR 01/09/19</b>	<u>APPROVED BY:</u> <b>BWIRKUS 01/09/19</b>
<i>TEMPLATE FILENAME: PRODUCT_SPEC(SIZE_A)(V.1).DOC</i>			

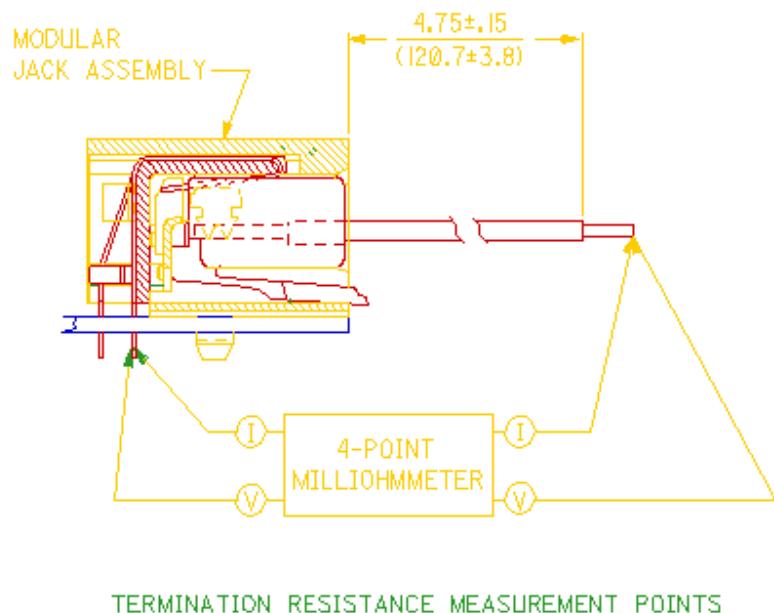


# PRODUCT SPECIFICATION

## 6.0 PACKAGING

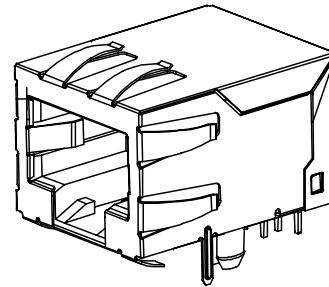
Parts shall be packaged to protect against damage during handling, transit and storage. See appropriate sales drawings on Sheet 1 for packaging descriptions.

## 7.0 GAGES AND FIXTURES



## 8.0 OTHER INFORMATION

REVISION:	ECR/ECN INFORMATION:	TITLE:	SHEET No.
<b>C</b>	EC No: UCR2004-0250 DATE: 2003/08/01	<b>PRODUCT SPECIFICATION ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACKS</b>	<b>5 of 5</b>
DOCUMENT NUMBER:	CREATED / REVISED BY:	CHECKED BY:	APPROVED BY:
<b>PS-43202-001</b>	<b>MKAMAR 01/09/19</b>	<b>MKAMAR 01/09/19</b>	<b>BWIRKUS 01/09/19</b>



## NOTES

- 1) MATERIAL:
  - HOUSING: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: SEE SHEETS 5 AND 6
  - INSULATOR: NYLON(PA), GLASS FILLED, UL94V-0, COLOR: SEE SHEETS 5 AND 6
  - TERMINALS: PHOSPHOR BRONZE, .012/(0.30) THICK
  - SHIELD: BRASS, .007/(0.18) THICK
- 2) FINISH:
  - TERMINALS:
    - A = SELECT GOLD IN CONTACT AREA: 50 MICROINCHES MIN.,
      - \*SELECT TIN IN PC TAIL AREA: 100 MICROINCHES MIN.,  
WITH OVERALL NICKEL UNDERPLATE: 50 MICROINCHES MIN.
    - B = SELECT GOLD IN CONTACT AREA: 30 MICROINCHES MIN.,
      - \*SELECT TIN IN PC TAIL AREA: 100 MICROINCHES MIN.,  
WITH OVERALL NICKEL UNDERPLATE: 50 MICROINCHES MIN.
  - \*THE PRIMARY SHIPPING CARTON WILL BE LABELED "COMPLIANT TO RoHS DIRECTIVE 2002/95/EC AND ELV ANNEX II OF DIRECTIVE 2000/53/EC".  
CARTONS WITHOUT THIS LABEL MAY CONTAIN PRODUCT WITH TIN-LEAD IN THE PC TAILS AND/OR SHIELD.
- SHIELD:
  - 100 MICROINCHES NICKEL OVER 50 MICROINCHES COPPER UNDERPLATE  
PCB GROUND TABS DIPPED IN TIN
- 3) PRODUCT SPECIFICATION AND PROCESSING PARAMETERS: PS-43202.
- 4) PACKAGING SPECIFICATION:
  - UNSHIELDED CONNECTOR ASSEMBLIES PACKAGED IN THERMOFORMED TRAYS  
PER MOLEX PACKAGING SPECIFICATION PK-43249-004.
  - UNSHIELDED CONNECTOR ASSEMBLIES PACKAGED IN TUBES  
PER MOLEX PACKAGING SPECIFICATION PK-43202-005.
  - SHIELDED CONNECTOR ASSEMBLIES PACKAGED IN THERMOFORMED TRAYS  
PER MOLEX PACKAGING SPECIFICATION PK-43202-004.
- 5) SEE SHEETS 7 - 9 FOR P.C. BOARD LAYOUTS.
- 6) CONFORMS TO FCC REGULATION PART 68.5 FOR MODULAR JACKS.

SHIELDED MODULAR JACK  
SHOWN WITH PANEL GROUND  
OPTION 'D' AND FRONT  
PCB GROUND TABS.

## DRAWING LEGEND

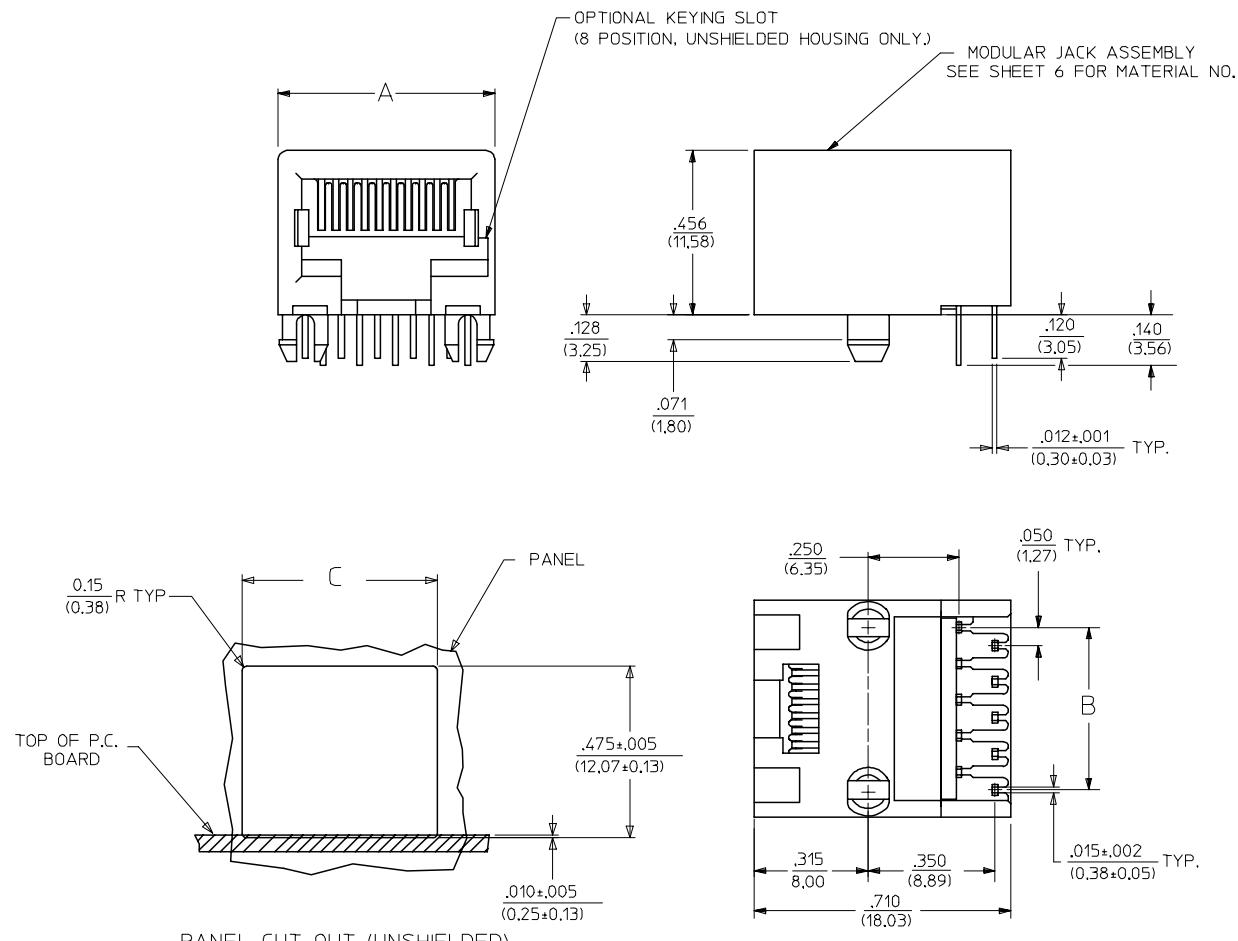
- SHEET 1- NOTES, DRAWING LEGEND
- SHEET 2- SHIELDED MODULAR JACK W/ BOTTOM GROUND TABS
- SHEET 3- SHIELDED MODULAR JACK W/O BOTTOM GROUND TABS
- SHEET 4- UNSHIELDED MODULAR JACK
- SHEET 5- PART NUMBER CHARTS
- SHEET 6- PART NUMBER CHARTS
- SHEET 7- FOOTPRINT LAYOUT FOR 4 POSITION HOUSING
- SHEET 8- FOOTPRINT LAYOUT FOR 6 POSITION HOUSING
- SHEET 9- FOOTPRINT LAYOUT FOR 8 POSITION HOUSING

ADDED PCB THICKNESS EC NO.: UCP2005-2739 DRAWN BY: SCHMIDT 2005/06/20 CHKD: APPR: FESMITH 2005/06/22 REV: 2	QUALITY SYMBOLS  = 0  = 0	GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE 4:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
		mm	INCH	DIMENSION STYLE IN/MM		TITLE ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACK ASSEMBLY	
4 PLACES $\pm$ ---		$\pm$ ---		DRAWN BY JTR		DATE 1993/03/31	
3 PLACES $\pm$ ---		$\pm$ .010		CHECKED BY JTR		DATE 1993/03/31	
2 PLACES $\pm$ .025		$\pm$ .014		APPROVED BY RAS		MATERIAL NO. SEE SHTS 5&6	
1 PLACE $\pm$ .036		$\pm$ ---		DOCUMENT NO. SDA-43202		SHEET NO. 1 OF 9	
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS							
THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION							

## UNSHIELDED MODULAR JACKS

SIZE 8, LOADED 10 SHOWN

HOUSING SIZE	LOADED CIRCUITS	A	B	C
4	4	.440 (11.18)	.150±.005 (3.81±0.13)	.460±.005 (11.68±0.13)
4	2	.440 (11.18)	.050±.005 (1.27±0.13)	.460±.005 (11.68±0.13)
6	6	.520 (13.21)	.250±.005 (6.35±0.13)	.540±.005 (13.72±0.13)
6	4	.520 (13.21)	.150±.005 (3.81±0.13)	.540±.005 (13.72±0.13)
6	2	.520 (13.21)	.050±.005 (1.27±0.13)	.540±.005 (13.72±0.13)
8	10	.600 (15.24)	.450±.005 (11.43±0.13)	.620±.005 (15.75±0.13)
8	8	.600 (15.24)	.350±.005 (8.89±0.13)	.620±.005 (15.75±0.13)
8	6	.600 (15.24)	.250±.005 (6.35±0.13)	.620±.005 (15.75±0.13)
8	4	.600 (15.24)	.150±.005 (3.81±0.13)	.620±.005 (15.75±0.13)
8	2	.600 (15.24)	.050±.005 (1.27±0.13)	.620±.005 (15.75±0.13)



SEE SHEET ONE		GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE 4:1	DESIGN UNITS INCH	④ ⑤ THIRD ANGLE PROJECTION	REVISE ON CAD ONLY			
EC NO: UCP204-1194 DRWNL: SCHMIDT 2004/01/02 CHKD: LSCHMIDT 2004/01/05 APPR: RSMITH 2004/01/09		QUALITY SYMBOLS		mm	INCH	TITLE				
		 = 0		4 PLACES	± $\frac{.005}{.010}$	± ---	ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACK ASSEMBLY			
		 = 0		3 PLACES	± ---	± .010				
				2 PLACES	± .025	± .014	DRAWN BY JTR 1993/03/31			
				1 PLACE	± 0.36	± ---	CHECKED BY JTR 1993/03/31			
				ANGULAR ± 1/2°			APPROVED BY RAS 1993/03/31			
				DRAFT WHERE APPLICABLE			MATERIAL NO. SEE SHT 6	DOCUMENT NO. SDA-43202	SHEET NO. 4 OF 9	
				MUST REMAIN WITHIN DIMENSIONS			THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
K2		REV								

## J SHIELDED MODULAR JACKS W/O BOTTOM GROUND TABS (SHEET 3)

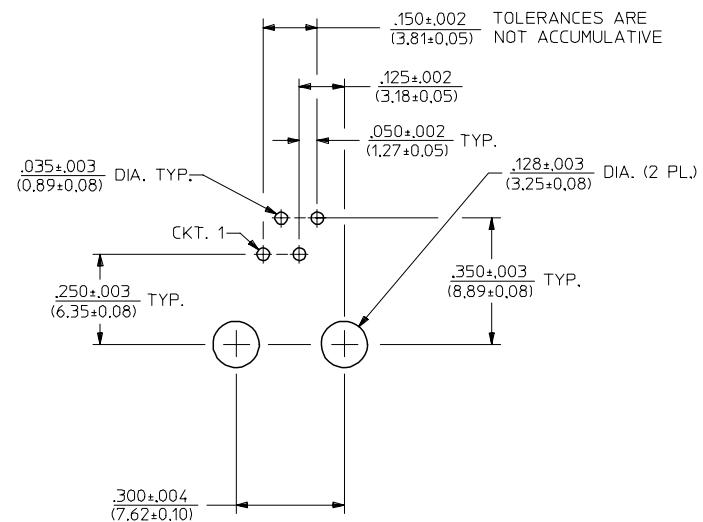
ASSEMBLY MATERIAL NUMBER	CONNECTOR SIZE	NUMBER OF CIRCUITS	SIDE/TOP PANEL GROUND TAB OPTION	PCB GROUND TAB OPTION	TERMINAL PLATING OPTION	PACKAGING	HOUSING COLOR
43202-8800	8	10	E	FRONT POSITION	A	TRAY	BLACK
43202-8801	8	10	F	FRONT POSITION	A	TRAY	
43202-8802	8	10	G	FRONT POSITION	A	TRAY	
43202-8803	8	10	H	FRONT POSITION	A	TRAY	
43202-8808	8	10	E	REAR POSITION	A	TRAY	
43202-8809	8	10	F	REAR POSITION	A	TRAY	
43202-8810	8	10	G	REAR POSITION	A	TRAY	
43202-8811	8	10	H	REAR POSITION	A	TRAY	
43202-8816	8	8	E	FRONT POSITION	A	TRAY	
43202-8817	8	8	F	FRONT POSITION	A	TRAY	
43202-8818	8	8	G	FRONT POSITION	A	TRAY	
43202-8819	8	8	H	FRONT POSITION	A	TRAY	
43202-8824	8	8	E	REAR POSITION	A	TRAY	
43202-8825	8	8	F	REAR POSITION	A	TRAY	
43202-8826	8	8	G	REAR POSITION	A	TRAY	
43202-8827	8	8	H	REAR POSITION	A	TRAY	

## J UNSHIELDED MODULAR JACKS- (SHEET 4)

ASSEMBLY MATERIAL NUMBER	CONNECTOR SIZE	NUMBER OF CIRCUITS	TERMINAL PLATING OPTION	KEYING OPTION	PACKAGING OPTION	HOUSING COLOR
43202-4101	4	4	A	NO	TRAY	BLACK
43202-4104	4	2	A	NO	TRAY	
43202-6101	6	6	A	NO	TRAY	
43202-6102	6	6	B	NO	TRAY	
43202-6104	6	4	A	NO	TRAY	
43202-6105	6	4	B	NO	TRAY	
43202-6107	6	2	A	NO	TRAY	
43202-6110	6	6	B	NO	TUBE	
43202-6113	6	2	A	NO	TRAY	
43202-6116	6	2	A	NO	TRAY	
43202-8101	8	10	A	NO	TRAY	
43202-8104	8	8	A	NO	TRAY	
43202-8105	8	8	B	NO	TRAY	
43202-8113	8	8	A	YES	TRAY	BLACK
43202-8116	8	6	A	NO	TRAY	
43202-8119	8	4	A	NO	TRAY	
43202-8122	8	2	A	NO	TRAY	
43202-8125	8	8	A	NO	TRAY	
43202-8504	8	8	A	NO	TUBE	
43202-8513	8	8	A	YES	TUBE	BLACK

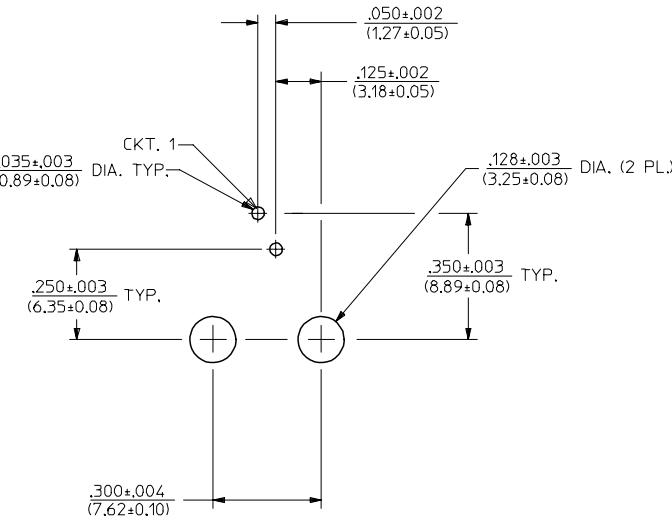
SEE SHEET ONE	GENERAL TOLERANCES (UNLESS SPECIFIED)	SCALE 4:1	DESIGN UNITS INCH	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY	
EC NO: UCP2004-1347 DRAWN/MKANARA2004/02/09 CHKD/MKANARA2004/02/09 APPR/BWIKUS 2004/02/10 REV L	4 PLACES $\pm$ ---	mm	INCH	DIMENSION STYLE IN/MM	TITLE ULTRA LOW PROFILE RIGHT ANGLE MODULAR JACK ASSEMBLY	
	3 PLACES $\pm$ ---	$\pm$ .010		DRAWN BY JBS	DATE 2000/10/03	
	2 PLACES $\pm$ 0.25	$\pm$ .014		CHECKED BY BW	DATE 2000/10/03	
	1 PLACE $\pm$ 0.36	$\pm$ ---	ANGULAR $\pm$ 1/2°			
	DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS			APPROVED BY BW	DATE 2000/10/03	
				MATERIAL NO. SEE CHART	DOCUMENT NO. SDA-43202	
				SHEET NO. 6 OF 9		
	THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION					C

13 12 11 10 9 8 7 6 5 4 3 2 43202



PC BOARD LAYOUT FOR 4 POSITION HOUSING

(4 CIRCUIT FOOT PRINT SHOWN)  
(COMPONENT SIDE OF BOARD)



PC BOARD LAYOUT FOR 4 POSITION HOUSING

(2 CIRCUIT FOOT PRINT SHOWN)  
(COMPONENT SIDE OF BOARD)

NOTES:  
1. RECOMMENDED PCB THICKNESS: .062±.005/(1.57±0.13)

ADD PCB THICKNESS		GENERAL TOLERANCES (UNLESS SPECIFIED)		SCALE 4:1	DESIGN UNITS IN/MM	THIRD ANGLE PROJECTION	REVISE ON CAD ONLY
EC NO: UCF2005-2739	DRAWN BY SCHMIDT 2005/06/20	4 PLACES	± ---	± ---	DRAWN BY JTR	DATE 1993/03/31	TITLE ULTRA LOW PROFILE
DOWNLOADED BY CHKOELHAG	2005/06/22	3 PLACES	± ---	± .010	DATE		RIGHT ANGLE MODULAR JACK
APPR'D BY SMITH	2005/06/23	2 PLACES	± 0.25	± .014	CHECKED BY JTR	DATE 1993/03/31	ASSEMBLY
		1 PLACE	± 0.36	± ---	ANGULAR ±1/2°	JTR	MOLEX MOLEX INCORPORATED
					DRAFT WHERE APPLICABLE	APPROVED BY RAS	MATERIAL NO. SDA-43202
					MUST REMAIN WITHIN DIMENSIONS	DATE 1993/03/31	DOCUMENT NO. SEE SHT 6
							SHEET NO. 7 OF 9
							THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION