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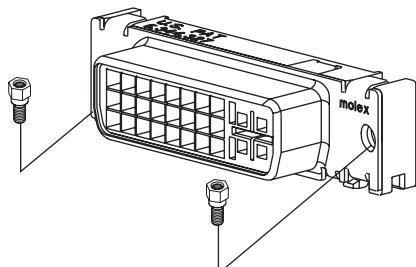
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Jameco Part Number 1976902

# MicroCross™ DVI-A Analog Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Vertical



### Features and Benefits

- Supports analog signal and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface compliant
- The MicroCross coaxial section supports a high bandwidth up to 2.5 GHz analog signal
- Selectively loaded circuits reduce cost

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: 88741, 74323 and other plug components

Designed In: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area  
and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

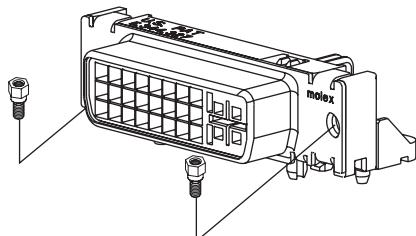
| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-3008</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-3009</a> | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

# MicroCross™ DVI-A Analog Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Right Angle



### Features and Benefits

- Supports analog signal and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface compliant
- The MicroCross coaxial section supports a high bandwidth up to 2.5 GHz analog signal
- Selectively loaded circuits reduce cost

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: 88741, 74323 and other plug components

Designed In: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area  
and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

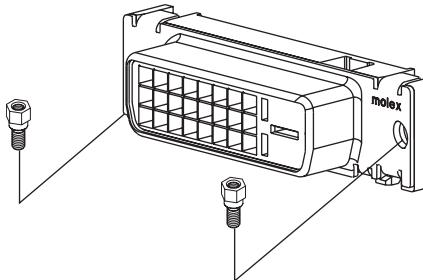
| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-1008</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-1009</a> | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

# MicroCross™ DVI-D Digital Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Vertical



| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-5000</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-5004</a> | Gold Flash      | Yes       |

### Features and Benefits

- Supports digital signals and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- Supports 4.95 Gbps over a single link and 9.9 Gbps performance over a dual link implementation
- Narrow key slot for polarization

### Reference Information

Product Specification: PS-74320-001  
 Packaging: Tray  
 UL File No.: E29179  
 CSA File No.: LR19980  
 Mates with: Versions of 88741, 74323 and other plug components  
 Designed In: Inches

### Electrical

Voltage: 40V  
 Current: 3.0A  
 Contact Resistance: 20 milliohms max.  
 Dielectric Withstanding Voltage: 500V  
 Insulation Resistance: 1,000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.  
 Insertion Force to PCB: 10 lb. max.  
 Mating Force: 10 lb. max.  
 Unmating Force: 2.2 lb. min.  
 Durability: 100 cycles

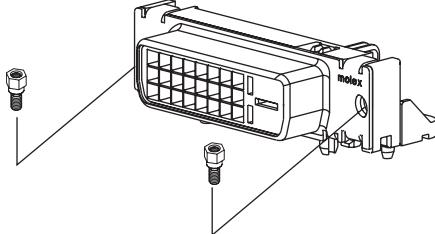
### Physical

Housing: Glass-filled thermoplastic, UL 94V-0  
 Contact: Copper Alloy  
 Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
 Shields—100 $\mu$ " bright Tin over Copper overall  
 Operating Temperature: -20 to +85°C

# MicroCross™ DVI-D Digital Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Right Angle



| Order No.                  | Plating         | Lead-Free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-4000</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-4004</a> | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

### Features and Benefits

- Supports digital signals and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- Supports 4.95 Gbps over a single link and 9.9 Gbps performance over a dual link implementation
- Narrow key slot for polarization

### Reference Information

Product Specification: PS-74320-001  
 Packaging: Tray  
 UL File No.: E29179  
 CSA File No.: LR19980  
 Mates With: Versions of 88741, 74323 and other plug components  
 Designed In: Inches

### Electrical

Voltage: 40V  
 Current: 3.0A  
 Contact Resistance: 20 milliohms max.  
 Dielectric Withstanding Voltage: 500V  
 Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.  
 Insertion Force to PCB: 10 lb. max.  
 Mating Force: 10 lb. max.  
 Unmating Force: 2.2 lb. min.  
 Durability: 100 cycles

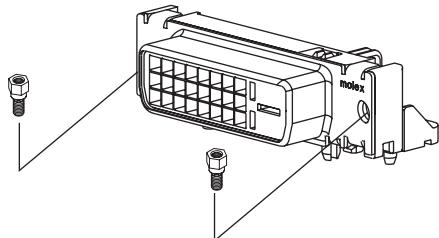
### Physical

Housing: Glass-filled thermoplastic, UL 94V-0  
 Contact: Copper Alloy  
 Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
 Shields—100 $\mu$ " bright Tin over Copper overall  
 Operating Temperature: -20 to +85°C

# MicroCross™ DVI-D Digital Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Right Angle ATX with Forklocks



### Features and Benefits

- Supports digital signals and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- Supports 4.95 Gbps over a single link and 9.9 Gbps performance over a dual link implementation
- Narrow key slot for polarization

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: Versions of 88741, 74323 and other  
plug components

Designed In: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1,000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area  
and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

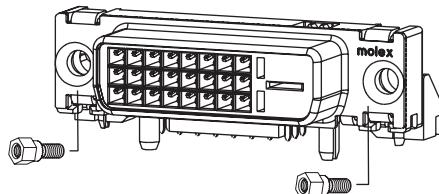
| Order No.  | Plating         | Lead-free |
|------------|-----------------|-----------|
| 74320-9000 | 30 $\mu$ " Gold |           |
| 74320-9004 | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

# MicroCross™ DVI-D Digital Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Extended Height Right Angle



### Features and Benefits

- Supports digital signals and offers excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- Supports 4.95 Gbps over a single link and 9.9 Gbps over a dual link implementation
- Narrow key slot for polarization

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: Versions of 88741, 74323 and other  
plug components

Designed in: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area  
and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

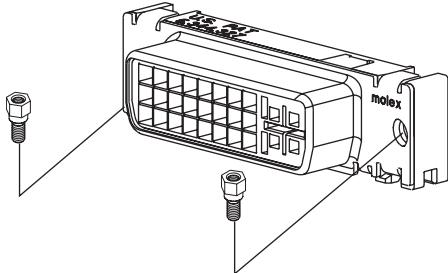
| Order No.  | Plating         | Lead-free |
|------------|-----------------|-----------|
| 74320-2010 | Gold Flash      |           |
| 74320-2011 | 30 $\mu$ " Gold | Yes       |

Please contact Molex for additional tail length options

# MicroCross™ DVI-I Digital/Analog Visual Interface Receptacle Header and Hardware

**74320**

**Panel Mount Through Hole  
Vertical**



## Features and Benefits

- Supports both analog and digital signals
- Plug and play interface
- Excellent EMI/RFI performance
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- The digital section supports 4.95 Gbps over a single link and 9.9 Gbps over a dual link.
- The MicroCross coax section supports a high-bandwidth of up to 2.5 GHz analog signal

## Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: 88741, 74323 and other plug components

Designed In: Inches

## Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

## Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

## Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall

Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-3000</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-3004</a> | Gold Flash      | Yes       |

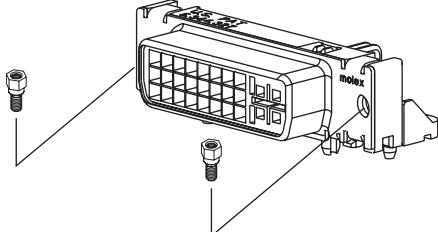
Please contact Molex for additional tail length options

# MicroCross™ DVI-I Digital/Analog Visual Interface

## Receptacle Header and Hardware

**74320**

**Panel Mount Through Hole  
Right Angle**



## Features and Benefits

- Supports both analog and digital signals
- Plug and play interface
- Excellent EMI/RFI performance
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- The digital section supports 4.95 Gbps over a single link and 9.9 Gbps over a dual link.
- The MicroCross coax section supports a high-bandwidth of up to 2.5 GHz analog signal

## Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates With: 88741, 74323 and other plug components

Designed In: Inches

## Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

## Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

## Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall

Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

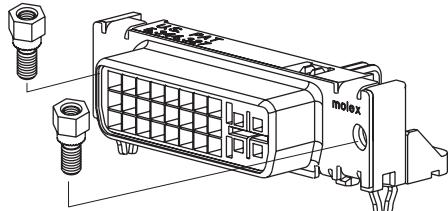
| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-1000</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-1004</a> | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

# MicroCross™ DVI-I Digital/Analog Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount Through Hole Right Angle with Forklocks



| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-9010</a> | 30 $\mu$ " Gold |           |
| <a href="#">74320-9014</a> | Gold Flash      | Yes       |

Please contact Molex for additional tail length options

### Features and Benefits

- Supports both analog and digital signals
- Excellent EMI/RFI performance
- Plug and play interface
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- The digital section supports 4.95 Gbps over a single link and 9.9 Gbps over a dual link
- The MicroCross coax section supports a high-bandwidth of up to 2.5 GHz analog signal

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates with: 88741, 74323 and other plug components

Designed In: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

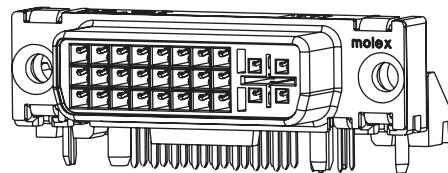
Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C

# MicroCross™ DVI-I Digital/Analog Visual Interface Receptacle Header and Hardware

**74320**

## Panel Mount, Through Hole, Extended Height Right Angle



| Order No.                  | Plating         | Lead-free |
|----------------------------|-----------------|-----------|
| <a href="#">74320-2020</a> | Gold Flash      |           |
| <a href="#">74320-2021</a> | 30 $\mu$ " Gold | Yes       |

Contact Molex for additional tail length options.

### Features and Benefits

- Supports both analog and digital signals
- Plug and play interface
- Excellent EMI/RFI performance
- LFH™ contact design is rugged and reliable
- DDWG DVI standard interface
- The digital section supports 4.95 Gbps over a single link and 9.9 Gbps over a dual link
- The MicroCross coax section supports a high-bandwidth of up to 2.5 GHz analog signal

### Reference Information

Product Specification: PS-74320-001

Packaging: Tray

UL File No.: E29179

CSA File No.: LR19980

Mates With: 88741, 74323 and other plug components

Designed In: Inches

### Electrical

Voltage: 40V

Current: 3.0A

Contact Resistance: 20 milliohms max.

Dielectric Withstanding Voltage: 500V

Insulation Resistance: 1000 Megohms min.

### Mechanical

Contact Retention to Housing: 1 lb. min.

Insertion Force to PCB: 10 lb. max.

Mating Force: 10 lb. max.

Unmating Force: 2.2 lb. min.

Durability: 100 cycles

### Physical

Housing: Glass-filled thermoplastic, UL 94V-0

Contact: Copper Alloy

Plating: Contact—30 $\mu$ " Gold or Gold flash in contact area and 100 $\mu$ " or 150 $\mu$ " Tin in tail area over Nickel overall  
Shields—100 $\mu$ " bright Tin over Copper overall

Operating Temperature: -20 to +85°C



## PRODUCT SPECIFICATION



## LANGUAGE

## ENGLISH

## 1.0 Scope

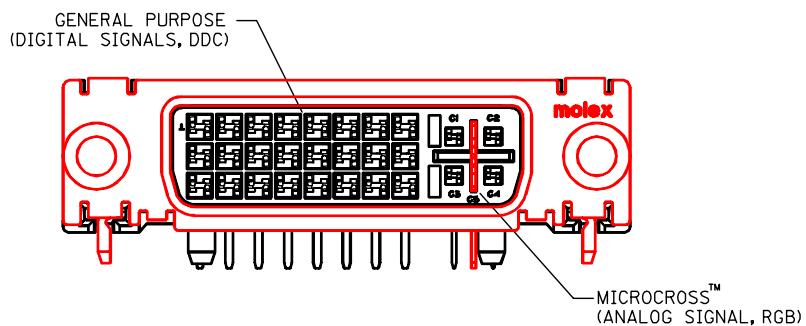
This specification covers the Molex MicroCross™ - Digital Visual Interface (DVI) system which includes cable plugs and board mount receptacles (Right Angle and Vertical).

The Digital Visual Interface connector system supports both analog and digital video transmission.

This specification covers the DVI cable to board, I/O connector system with requirements as set forth by Molex Incorporated.

## 2.0 Product Description

The MicroCross™ DVI system is designed to meet the industry's requirements for analog and digital computer monitors. There are (2) different receptacle connectors which correspond to the video support present on the host system (mother board/graphics cards). The DVI-D (Digital) receptacle connector supports hosts systems that transmit digital video. The DVI-I (Intergrated) receptacle connector supports host systems that are enabled to transmit both analog and digital video. This is achieved by utilizing two different sets of contacts as shown in Figure 1 below:



*Figure 1: Two sets of contacts (DVI-I Shown)*

|                                |  |        |   |             |   |           |                         |   |  |             |    |              |    |    |                          |    |                    |    |   |   |
|--------------------------------|--|--------|---|-------------|---|-----------|-------------------------|---|--|-------------|----|--------------|----|----|--------------------------|----|--------------------|----|---|---|
|                                | REV  | J      | J | J           | J | J         | J                       | J   | J  | J           | J  | J            | J  | J  | J                        | J  | J                  | J  | J | J |
|                                | SHT  | 1      | 2 | 3           | 4 | 5         | 6                       | 7   | 8  | 9           | 10 | 11           | 12 | 13 | 14                       | 15 | 16                 | 17 |   |   |
| REVISE ON PC ONLY              |  |        |   |             |   |           |                         | TITLE   | MicroCross™ - DVI<br>I/O Plug and Receptacle<br>Connector System |             |    |              |    |    |                          |    |                    |    |   |   |
| J                              | REVISED<br>PER EC# T2003-0134<br>TONY ZHANG 02/11/22 |        |   |             |   |           |                         |   |  |             |    |              |    |    |                          |    |                    |    |   |   |
| REV                            | DESCRIPTION  |        |   |             |   |           |                         | THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO<br>MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION |  |             |    |              |    |    |                          |    |                    |    |   |   |
| DESIGN CONTROL<br>UDT          |  |        |   | STATUS<br>M |   |           | WRITTEN BY:<br>SCHMIDGA |   | CHECKED BY:  |             |    | APPROVED BY: |    |    | DATE: YR / MO / DAY      |    |                    |    |   |   |
| DOCUMENT NO.<br>  PS-74320-001 |  |        |   |             |   |           |                         |   |  |             |    |              |    |    | FILE NAME<br>PS74320.LWP |    | SHT NO.<br>1 OF 17 |    |   |   |
| ES-40000-3996                  |  | REV. A |   | SHEET 3     |   | 95/MAR/10 |                         | EC U5-0926  |  | DCBRD03.SAM |    |              |    |    |                          |    |                    |    |   |   |



## PRODUCT SPECIFICATION



LANGUAGE

ENGLISH

### 1. General purpose signals:

Terminals: 24 circuits on a 0.075 inch/1.91 mm grid

Signals: Includes power, grounds, digital and video signals, analog synch lines and DDC (Display Data Channel) signals.

| Pin | Signal Assignment       | Pin | Signal Assignment        | Pin | Signal Assignment        |
|-----|-------------------------|-----|--------------------------|-----|--------------------------|
| 1   | T.M.D.S. Data2-         | 9   | T.M.D.S. Data1-          | 17  | T.M.D.S. Data 0-         |
| 2   | T.M.D.S. Data2+         | 10  | T.M.D.S. Data1+          | 18  | T.M.D.S. Data 0+         |
| 3   | T.M.D.S. Data2/4 Shield | 11  | T.M.D.S. Data 1/3 Shield | 19  | T.M.D.S. Data 0/5 Shield |
| 4   | T.M.D.S. Data 4-        | 12  | T.M.D.S. Data 3-         | 20  | T.M.D.S. Data 5-         |
| 5   | T.M.D.S. Data 4+        | 13  | T.M.D.S. 3+              | 21  | T.M.D.S. Data 5+         |
| 6   | DDC Clock               | 14  | +5 V Power               | 22  | T.M.D.S. Clock Shield    |
| 7   | DDC Data                | 15  | Ground (for +5V)         | 23  | T.M.D.S. Clock+          |
| 8   | No Connect              | 16  | Hot Plug Detect          | 24  | T.M.D.S. Clock-          |

*Table 1: Digital-Only Connector Pin Assignments*

Source: Digital Visual Interface Specification, Revision 1.0

### 2. MicroCross™:

#### a) Plug and Receptacle - I - Intergrated analog/digital - see figure 3, sheet 4

Terminals: 4 circuits on a 0.100 inch/2.54 mm grid with a crossing ground plane in between.

Signals: High frequency, 75 ohm, analog video

#### b) Plug and Receptacle - D - Digital Version

Terminals: A single key on the plug and corresponding slot on the receptacle.

Signals: The key is used for mechanical polarization only, it does not carry any electrical signals.

|   |                   |             |  |   |       |  |
|---|-------------------|-------------|--|---|-------|--|
|   | REVISE ON PC ONLY |             | TITLE<br><br>MiroCross™ - DVI<br>I/O Plug and Receptacle<br>Connector System | THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO<br>MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION |       |  |
|   | J                 | SEE SHEET 1 |  |   |       |  |
|   | REV               | DESCRIPTION |  |   |       |  |
| DOCUMENT NO.  |                   |             |  | FILE NAME   | SHEET |  |
| PS-74320-001  |                   |             |  | PS74320.LWP   | 2     |  |
| ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM |                   |             |  |   |       |  |



## PRODUCT SPECIFICATION



LANGUAGE

ENGLISH

| Pin | Signal Assignment        | Pin | Signal Assignment                               | Pin | Signal Assignment        |
|-----|--------------------------|-----|---|-----|--------------------------|
| 1   | T.M.D.S. Data 2-         | 9   | T.M.D.S. Data 1-                                | 17  | T.M.D.S. Data 0-         |
| 2   | T.M.D.S. Data 2+         | 10  | T.M.D.S. Data 1+                                | 18  | T.M.D.S. Data 0+         |
| 3   | T.M.D.S. Data 2/4 Shield | 11  | T.M.D.S. Data 1/3 Shield                        | 19  | T.M.D.S. Data 0/5 Shield |
| 4   | T.M.D.S. Data 4-         | 12  | T.M.D.S. Data 3-                                | 20  | T.M.D.S. Data 5-         |
| 5   | T.M.D.S. Data 4+         | 13  | T.M.D.S. Data 3+                                | 21  | T.M.D.S. Data 5+         |
| 6   | DDC Clock                | 14  | +5V Power                                       | 22  | T.M.D.S. Clock Shield    |
| 7   | DDC Data                 | 15  | Ground<br>(return for +5V, HSync,<br>and VSync) | 23  | T.M.D.S. Clock+          |
| 8   | Analog Vertical Sync     | 16  | Hot Plug Detect                                 | 24  | T.M.D.S. Clock-          |
| C1  | Analog Red               | C2  | Analog Green                                    | C3  | Analog Blue              |
| C4  | Analog Horizontal Sync   | C5  | Analog Ground<br>(analog R, G, & B return)      |     |                          |

Table 2: Combined Analog and Digital Connector Pin Assignments

Source: Digital Visual Interface, Revision 1.0

Additional general specifications are:

Plug:

- LFH (Low Force Helix) style contacts
- fully shielded RFI/EMI can
- grounding detents on mating shell
- solder tails for cable termination
- positive retention jackscrew: thread 4-40 UNC-2A

Receptacle:

- high cycle, dual beam, LFH shrouded contacts
- polarization achieved by a "D" shaped housing/shield
- single piece shield with integral ground leg
- shield protrudes for ESD considerations
- solder tails for thru hole board mount
- plastic retention pegs
- jackposts: # 4-40 UNC-2A&B threads. The recommended application torque setting is 4 lbf in maximum. To prevent stripping the shield threads while installing the jackposts, it is recommended the jackposts are started by hand or with a lower initial torque driver setting. The engaged threads are rated to hold a minimum of 5 lbf in of torque.

|   |                   |             |  |   |       |  |
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## PRODUCT SPECIFICATION



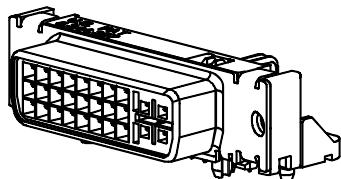
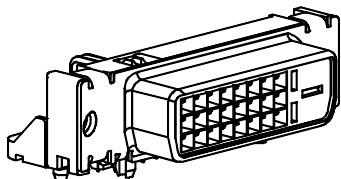
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### 2.1 Product Drawing Numbers

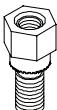
#### 2.1.1 Receptacle:

The DVI receptacle is for systems which support digital video (DVI-D) or both analog and digital video (DVI-I).



*Figure 2:*  
*Right Angle DVI-D version*  
*(Digital)*

*Figure 3:*  
*Right Angle DVI-I version*  
*Intergrated(Analog/Digital)*



*Figure 4: Jackpost*

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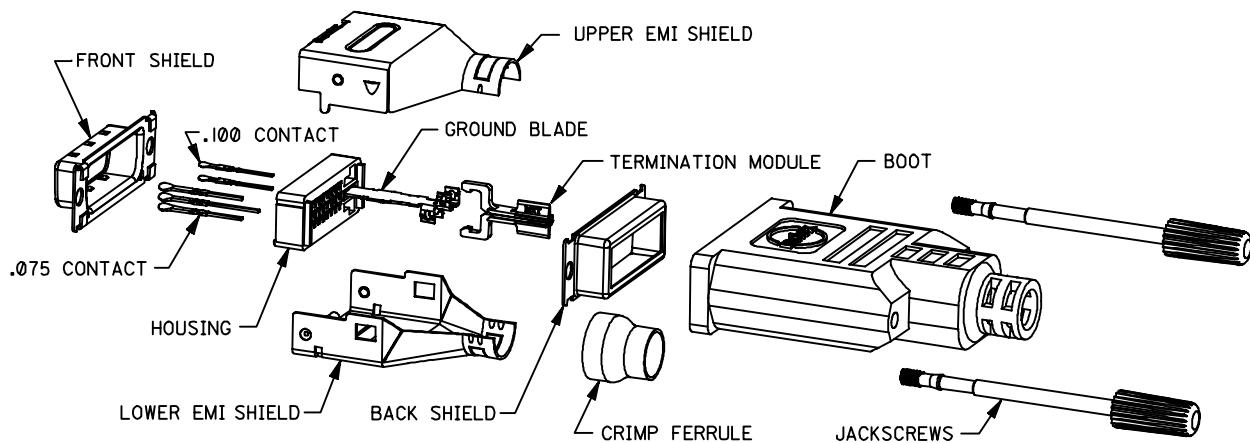


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### 2.1.2 DVI Plug

The DVI plug is for systems which use analog or digital video. The analog DVI plug shown below supports analog video transmission from the host to the display.



*Figure 5:*  
*Analog Version*

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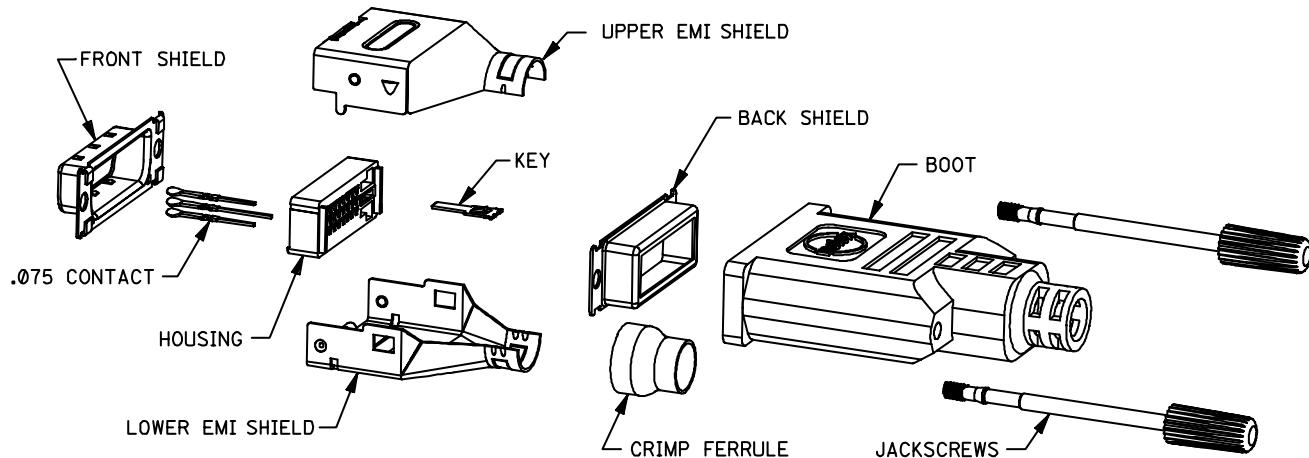


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### 2.1.3

The digital DVI plug shown below supports digital video transmission from the host to the display.



*Figure 6:  
Digital Version*

### 2.2 Safety Agency Approvals

UL File Number ..... E29179, Volume 10, Section 12  
CSA File Number ..... LR19980

### 3.0 Applicable Documents and Specifications

3.1 All documents referenced shall be of the latest revision. The order of precedence detailing requirements of this specification is as follows:

1. Product Drawings
2. This specification

### 3.2 Reference Documents

3.2.1 EIA RS-364-(06,09,13,17,18,20,21,23,27,28,31,32,41,46,65,67,70,90) Electronic Industries Association, Recommended Standard

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- 3.2.2 IEC-801-2 International Electrotechnical Commission, Electrostatic Discharge Requirements
- 3.2.3 MIL STD-202: Test methods for electronics and electrical component parts
- 3.2.4 Molex PS-74320-9999 Application Specification, DVI Plug Cable Assembly
- 3.2.5 Molex ES-74320-9998 Termination Specification, DVI Cable Assemblies
- 3.2.6 Molex PS-74320-9997 Cable Assembly Specification
- 3.2.7 UL 94: Tests for flammability of plastics materials

## 4.0 Ratings

## 4.1 Voltage

40 Volts AC (RMS)

## 4.2 Current

3.0 Amps per circuit.

30 °C maximum temperature rise and 55 °C maximum ambient per EIA-364-70.

### 4.3 Temperature

Operating: - 20 °C to + 85 °C

Nonoperating: - 20 °C to + 85 °C

|   |                   |  |       |   |            |  |  |
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### 5.0 Performance

#### 5.1 Electrical Performance

| ITEM                                      | TEST CONDITION  | REQUIREMENT   |
|---|---|---|
| Contact Resistance                        | Bulk resistance measured between plug solder tails and receptacle solder tails per ANSI/EIA-364-23  | 20 milliohm maximum, initial per contact mated pair<br>10 milliohm maximum change from initial reading per contact mated pair |
| Shell Resistance                          | Bulk resistance measured between ground leg on receptacle shield and the plug cable braid. Test current=100mA; Test voltage=5 Volts DC open circuit maximum per ANSI/EIA-364-06A-83   | 50 milliohm maximum initial<br>50 milliohm maximum change from initial reading  |
| Insulation Resistance                     | Test voltage = 500 Volts DC +/- 50 V<br>Unmated and Unmounted per ANSI/EIA 364-21, Method C   | 1Gigaohm Minimum between adjacent contacts and contacts and shell   |
| Dielectric Withstanding Voltage           | Test voltage = 500 Volts DC +/-50 V<br>Unmated and Unmounted per ANSI/EIA 364-20, Method C<br>Barometric pressure of 15 psi   | No flashover, No sparkover, No excess leakage, No Breakdown   |
| Contact Current Rating                    | Maximum ambient = 55 degree C<br>Maximum temperature change = 85 degree C per ANSI/EIA-364-70, TP-70  | 3.0 A maximum   |
| Applied Voltage Rating                    |   | 40 Volts AC (rms) continuous maximum, on any signal pin with respect to the shield  |
| Electrostatic Discharge                   | Test unmated from 1 kV to 8kV in 1 kV steps using 8mm ball prob per IEC 801-2<br>Contact discharge to shell<br>Air discharge perpendicular to shell<br>Air discharge at angle to shell  | No evidence of discharge to contacts at 8kV. Discharge to the shell is acceptable.  |
| T.M.D.S. Signals<br>Time Domain Impedance | Risetime = 330 pS (10%-90%)<br>S:G ratio per DVI pin designation<br>Differential Measurement<br>Specimen Environment Impedance = 100 ohm differential<br>Source-side receptacle connector mounted on a controlled impedance pcb fixture per ANSI/EIA-364-108 draft Proposal | 100 ohms +/-15%   |

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|  |   |  |
|--|---|--|
| T.M.D.S. Signals<br>Time Domain<br>Crosstalk:<br>FEXT                    | Risetime = 330 pS (10%-90%)<br>S:G ratio per DVI pin designation<br>Differential Measurement<br>Specimen Environment Impedance = 100 ohm<br>differential<br>Source-side receptacle and the load side plug<br>connector are mounted on a controlled<br>impedance pcb fixture<br>(1) Driven pair and (1) victim pair<br>per ANSI/EIA-364-90 Draft Proposal  | 5% Maximum   |
| T.M.D.S. Signals<br>Rise Time Degradation                                | S:G ratio per DVI pin designation<br>Differential Measurement<br>Specimen Environment Impedance = 100 ohm<br>differential<br>Source-side receptacle and the load side plug<br>connector are mounted on a controlled<br>impedance pcb fixture<br>per ANSI/EIA-364-102 Draft Proposal   | 160 pS Maximum<br>(Note: Converted bandwidth using<br>BW=0.35/t rise yields 2.2 GHz) |
| Analog RGB<br>Coaxial Signals<br><br>Time<br>Domain Impedance            | Risetime = 700 pS (10%-90%)<br>S:G ratio per DVI pin designation<br>Single-ended Measurement<br>Specimen Environment Impedance = 75 ohm<br>single-ended<br>Source-side receptacle connector mounted on<br>a controlled impedance pcb fixture<br>per ANSI/EIA-364-108 Draft Proposal   | 75 ohms +/-10%   |
| Analog RGB<br>Coaxial Signals<br><br>Time<br>Domain Crosstalk:<br>(FEXT) | Risetime = 700 pS (10%-90%)<br>S:G ratio per DVI pin designation<br>Single-ended Measurement<br>Specimen Environment Impedance = 75 ohm<br>single-ended<br>Source-side receptacle connector is mounted<br>on a controlled impedance pcb fixture and the<br>load side plug connector is terminated to<br>semi-rigid coax.<br>(1) Driven line and (1) victim line<br>per ANSI/EIA-364-90 Draft Proposal | 3% Maximum   |
| Analog RGB<br>Coaxial Signals<br><br>Rise Time Degradation               | S:G ratio per DVI pin designation<br>Single-ended Measurement<br>Specimen Environment Impedance = 75 ohm<br>single-ended<br>Source-side receptacle connector is mounted<br>on a controlled impedance pcb fixture and the<br>load side plug connector is terminated to<br>semi-rigid coax.<br>per ANSI/EIA-364-102   | 140pS Maximum<br>(Note: Converted bandwidth using<br>BW=0.35/t rise yields 2.5 GHz)  |

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## PRODUCT SPECIFICATION



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### 5.2 Mechanical Performance

| ITEM                         | TEST CONDITION  | REQUIREMENT  |
|------------------------------|---|--|
| Mating Force                 | One pair per ANSI/EIA 364-13<br>Insertion speed: 1inch (25mm) per minute  | 10.0 lbf (4.5 kgf) maximum   |
| Unmating Force               | Mated pair per ANSI/EIA 364-13<br>Withdraw speed: 1inch (25mm) per minute   | 2.2 lbf (1.0 kgf) minimum<br>8.8 lbf (4.0 kgf) maximum   |
| Receptacle Contact Retention | Individual contact  | 1.0 lbf (0.45 kgf) minimum   |
| Receptacle Key Retention     | Individual key  | 2.0 lbf (0.90 kgf) minimum   |
| Plug Contact Retention       | Push out from mating face; Individual contact   | 10 lbf (4.5 kgf) minimum   |
| Plug Key Retention           | Push out from mating face; Individual key   | 10 lbf (4.5 kgf) minimum   |
| Durability                   | Automatic cycling: 100 cycles per ANSI/EIA 364-09<br><br>at 100 +/- 50 cycles per hour  | Contact Resistance per EIA 364-23:<br>10 milliohm maximum change from initial per contact pair<br>All samples to be mated<br>Shell Resistance: 50 milliohm maximum (change from initial reading)               |
| Vibration                    | 15 minutes / axis per ANSI/EIA 364-28, Method 5A  | No discontinuities at 1 microsecond or longer (each contact) when continuity is tested per EIA-364-46  |
| Shock (Mechanical)           | Per ANSI/EIA 364-27, Condition A (specified pulse)  | No discontinuities at 1 microsecond or longer (each contact) when continuity is tested per EIA-364-46  |
| Cable Pullout Force          | Test for cable strain relief & termination integrity. Cable subjected to 25.0 lbf (11.3 kgf) static load for one minute while monitoring continuity. Isolate plug & receptacle interface from load. | No discontinuities greater than 1 microsecond  |
| Board Insertion Force        |   | 10.0 lbf (4.5 kgf) maximum   |
| Cable Flex                   | 100 cycles in each of 2 planes<br>Dimension X=3.7x Cable Diameter per ANSI/EIA 364-41, Condition I  | No discontinuities greater than 1 microsecond allowed during flexing on contacts or shields per EIA-364-46<br>Dielectric Withstanding Voltage and Insulation Resistance tested per requirements of section 5.1 |
| Normal Force                 | For reference only  | .050" pitch terminals: 75 grams typical<br>.075" pitch terminals: 90 grams typical<br>Ground Plane: 100 grams typical  |
| Thread Torque                | Mounted to panel; Test to failure; Tighten jackposts with torque gage until threads are stripped and jackpost turns freely  | 5.0 lbf in (5.76 kgf cm) minimum   |

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### 5.3 Environmental Performance

| ITEM                      | TEST CONDITION   | REQUIREMENT  |
|---------------------------|--|--|
| Thermal Shock             | 10 cycles Mated/Unmated per ANSI/EIA 364-32, Condition I                         | Contact Resistance: <b>10</b> milliohm maximum change from initial per contact pair<br>All samples to be mated<br>Shell Resistance: <b>50</b> milliohm maximum change from initial per EIA-364-23                  |
| Humidity (Cyclic)         | ANSI/EIA 364-31, Conditions A and B<br>Method III, omit 7A and 7B                | Contact Resistance: <b>10</b> milliohm maximum change from initial per contact pair<br>All samples to be mated<br>Shell Resistance: <b>50</b> milliohm maximum change from initial per EIA-364-23                  |
| Thermal Aging             | 105 °C for 250 hours<br>Mated per ANSI/EIA 364-17, Condition 4, Method A.        | Contact Resistance: <b>10</b> milliohm maximum change from initial per contact pair<br>All samples to be mated<br>Shell Resistance: <b>50</b> milliohm maximum change from initial per contact pair per EIA-364-23 |
| Temperature Rise          | Per ANSI/EIA 364-70  | 30 °C maximum temperature rise   |
| Resistance to Solder Heat | Dip connector solder tails to board for 10 seconds<br>Solder Temp = 260 +/- 5 °C | No visual damage to insulator  |
| Solderability             | Per MIL-STD-202, Method 208  | 95% minimum coverage   |
| Temperature Rating        | Operating  | -20 degree C to +85 degree C   |
| Temperature Rating        | Non-Operating  | -20 degree C to +85 degree C   |

|   |                    |  |  |                          |  |  |
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### 6.0 Packaging

#### 6.1 Receptacles:

All receptacles are packaged in trays. For specific packaging information , refer to PK-74320-001 for right angle receptacles and PK-74320-002 for vertical receptacles.

### 7.0 Other Information

#### 7.1 Test Sequences

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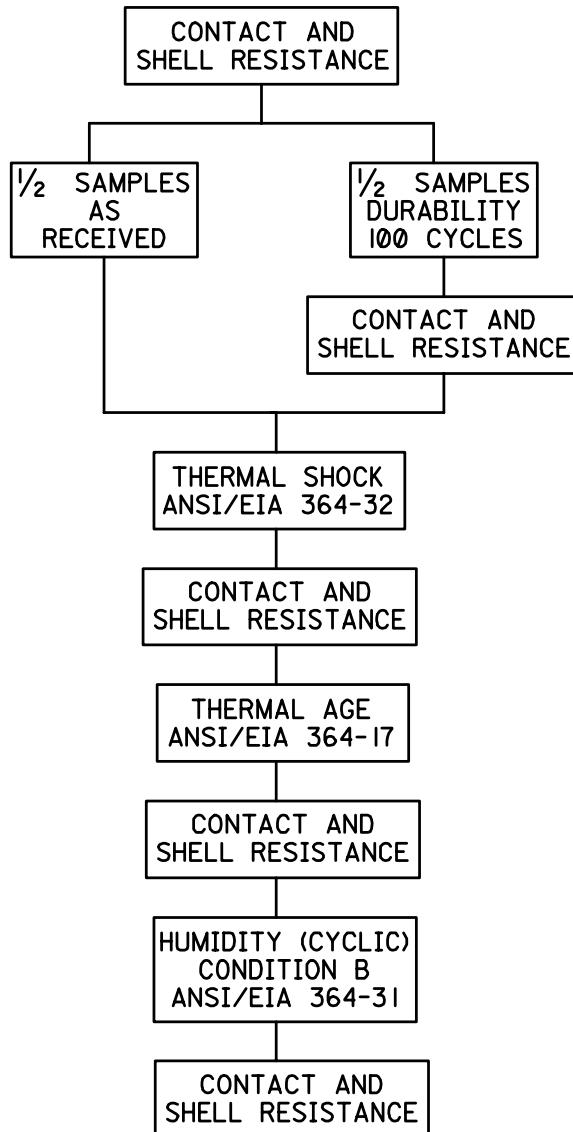
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## Group 1 : Mated Environmental



### Number of samples

(5) Receptacle assembled to printed circuit board.

(5) Cable assemblies with a plug assembled to one end, 10 inch/25.4 cm long

|   |                   |             |   |
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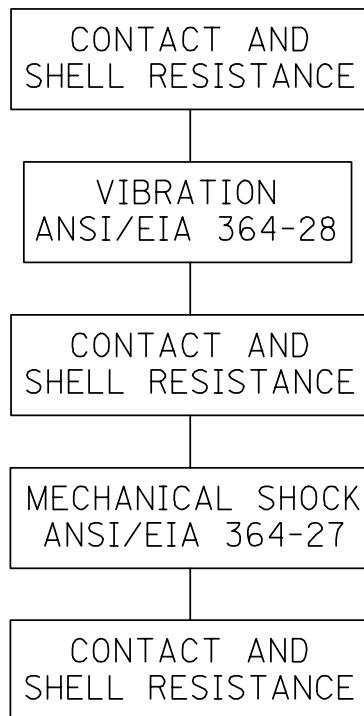
## PRODUCT SPECIFICATION



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### Group 2 : Mated Mechanical



#### Number of Samples:

- (2) Receptacles, assembled to printed circuit board.
- (2) Cable assemblies with a plug assembled to one end, 10 inch/25.4 cm long.

Note: Connector is to be mounted on a fixture that simulates the typical application. The receptacle connector shall be mounted to a panel, per the receptacle panel cutout shown in Figure 12, which is permanently affixed to the fixture. The plug shall be mated to the receptacle with jackscrews fully engaged and the other end of the cable shall be permanently clamped to the fixture, 3 inches from connector face.

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|--|-------------------|------------------------|--|-------------|--|
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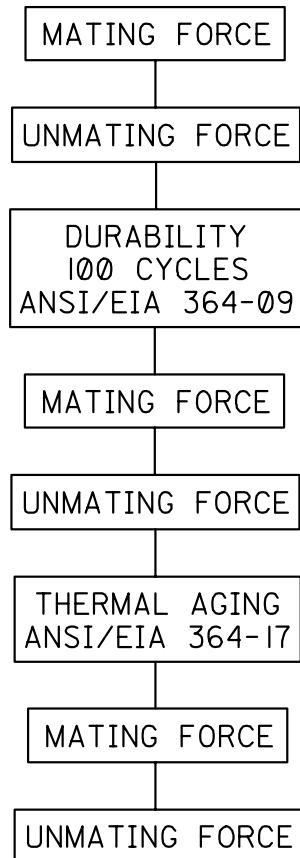
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## Group 3 : Mated Mechanical



### Number of Samples:

- (2) Receptacles, assembled to printed circuit board.
- (2) Cable assemblies with a plug assembled to one end, 10 inch/25.4 cm long.

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| PS-74320-001                           |                   |                        |   | PS74320.LWP |  |
| ES-40000-3996 REV. A SHEET 4 95/MAR/10 |                   | EC U5-0926 DCBRD03.SAM |   | SHEET 15    |  |



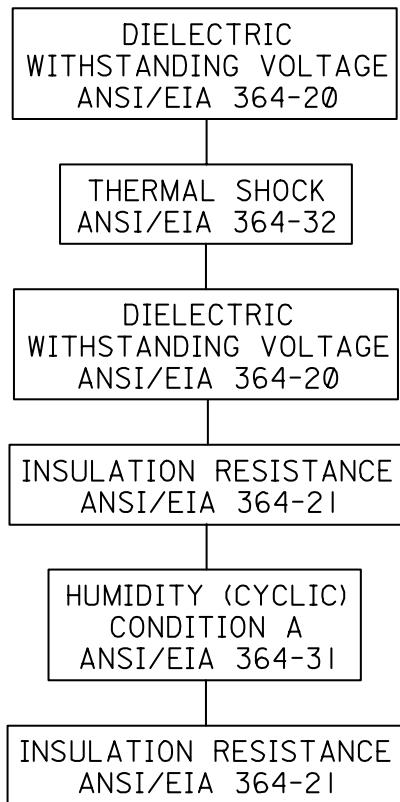
# PRODUCT SPECIFICATION



LANGUAGE

ENGLISH

## Group 4 : Insulator Integrity



### Number of Samples:

- (2) Receptacles, assembled to printed circuit board.
- (2) Cable assemblies with a plug assembled to one end, 10 inch/25.4 cm long

| REV   | REVISE ON PC ONLY |             | TITLE<br><br>MiroCross™ - DVI<br>I/O Plug and Receptacle<br>Connector System<br><br>THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO<br>MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION | FILE NAME   | SHEET |  |
|---|-------------------|-------------|---|-------------|-------|--|
|   | J                 | SEE SHEET 1 |   |             |       |  |
|   | DESCRIPTION       |             |   |             |       |  |
| DOCUMENT NO.  |                   |             |   | FILE NAME   | SHEET |  |
| PS-74320-001  |                   |             |   | PS74320.LWP | 16    |  |
| ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM |                   |             |   |             |       |  |



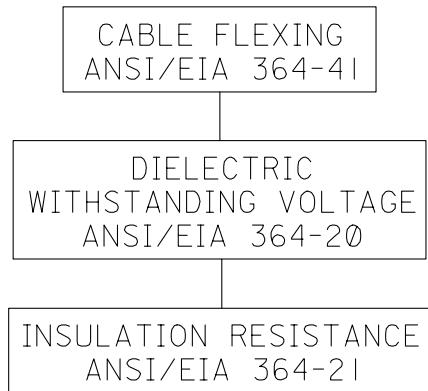
## PRODUCT SPECIFICATION



LANGUAGE

ENGLISH

### Group 5 : Cable Flexing



#### Number of Samples:

(2) Cable assemblies

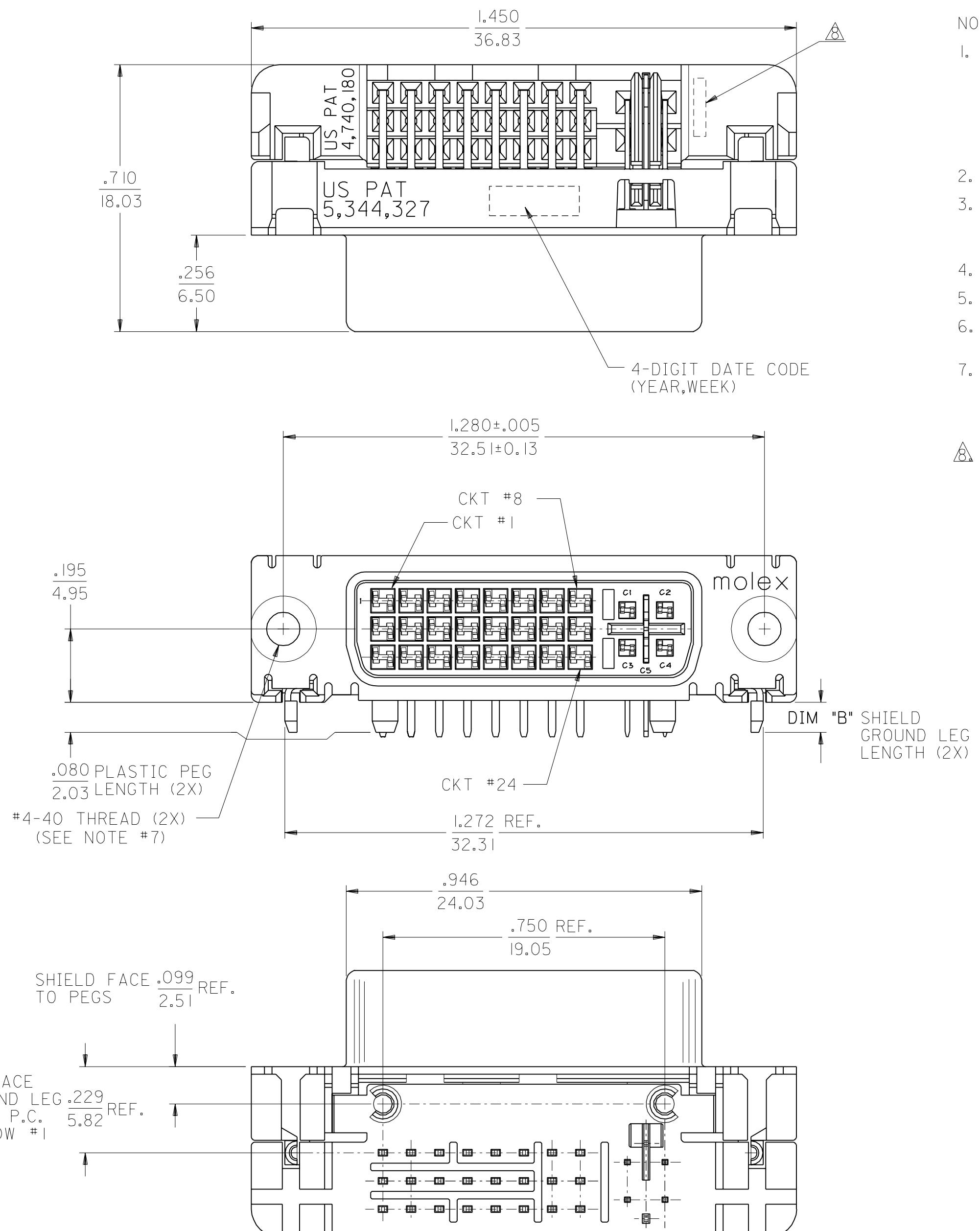
### Group 6: Electrostatic Discharge

ELECTROSTATIC  
DISCHARGE

#### Number of Samples:

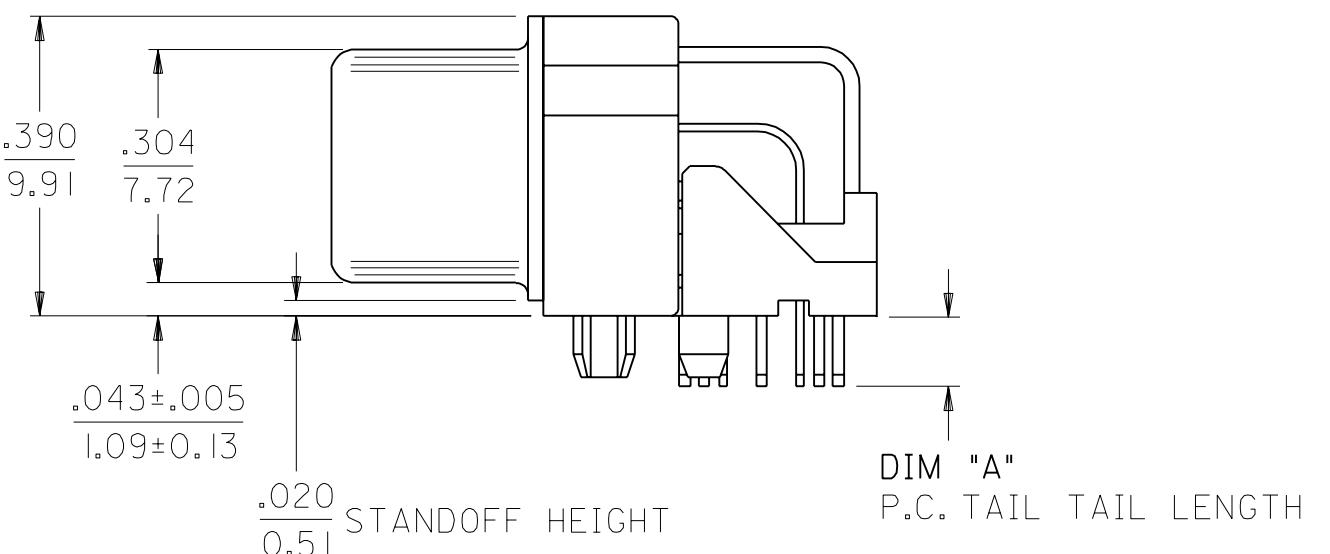
(1) Receptacle connector

| REV   | REVISE ON PC ONLY |             | TITLE<br><br>MiroCross™ - DVI<br>I/O Plug and Receptacle<br>Connector System<br><br>THIS DOCUMENT CONTAINS INFORMATION THAT IS PROPRIETARY TO<br>MOLEX INC. AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION | FILE NAME<br><br>PS74320.LWP | SHEET<br><br>17 |  |
|---|-------------------|-------------|---|------------------------------|-----------------|--|
|   | J                 | SEE SHEET 1 |   |                              |                 |  |
|   | REV               | DESCRIPTION |   |                              |                 |  |
| DOCUMENT NO.  |                   |             |   | FILE NAME                    | SHEET           |  |
| PS-74320-001  |                   |             |   | PS74320.LWP                  | 17              |  |
| ES-40000-3996 REV. A SHEET 4 95/MAR/10 EC U5-0926 DCBRD03.SAM |                   |             |   |                              |                 |  |

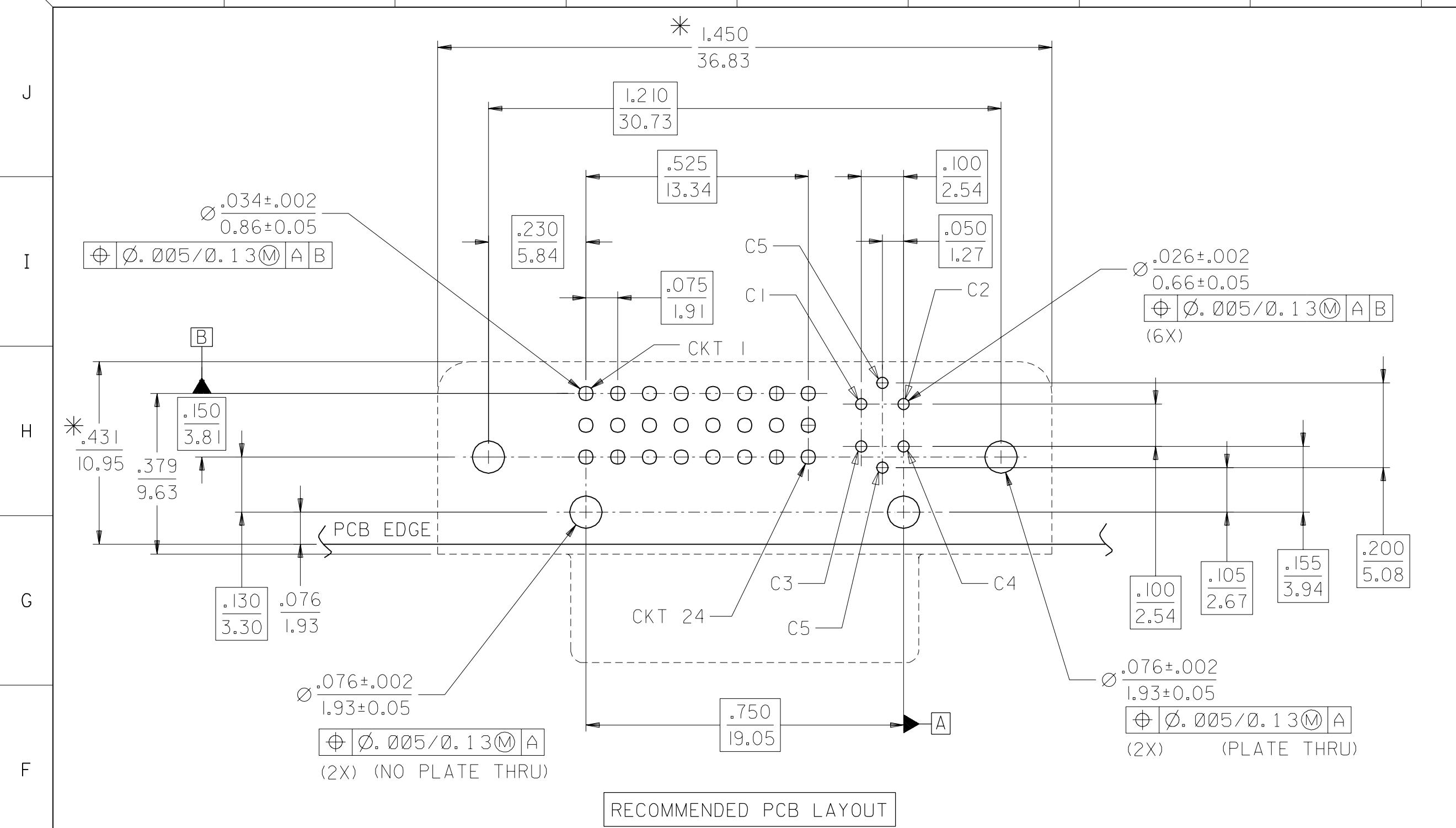


## NOTES:

- MATERIALS:  
SHIELD -- DEEP DRAW STEEL.  
HOUSING -- GLASS FILLED POLYMER, NATURAL (WHITE), UL 94v-0.  
TAIL ALIGNER -- GLASS FILLED POLYMER, NATURAL (WHITE), UL 94v-0.  
CONTACTS -- COPPER ALLOY.  
MICROCROSS GROUND -- COPPER ALLOY.
- PLATING: SEE SHEET 2
- PRODUCT CONFORMS TO MOLEX PRODUCT SPECIFICATION NUMBER PS-74320-001. REFER TO THIS SPECIFICATION FOR SPACING ON A P.C.B. OF TWO OR MORE CONNECTORS WHEN USING A CABLE OR AN ADAPTER ASSEMBLY.
- SEE SHEET 2 FOR RECOMMENDED PANEL CUT-OUT AND P.C.B. LAYOUT.
- PARTS WILL BE PACKAGED IN TRAYS PER PK-74320-010.
- PRODUCT CONFORMS TO DDWG DVI SPECIFICATION. REFER TO THIS SPECIFICATION FOR CONNECTOR MATING DIMENSIONS.
- RECOMMENDED MOUNTING HARDWARE IS MOLEX P/N 71781-0001 OR 82007-0300 OR 88780-6066.  
REFER TO PRODUCT SPECIFICATION NUMBER PS-74320-001 FOR THE APPLICATION TORQUE REQUIREMENT FOR ASSEMBLING MOUNTING HARDWARE.
- CAVITY ID AND MANUFACTURING PLANT CODE.



| PART NUMBER | PLATING OPTION | DIM "A"   | DIM "B"   | MODIFICATION<br>EC NO. SH2004-0130<br>DRWN: DXUE 04/03/04<br>CHR: HARVEY 04/03/11<br>APPR: SHIANG 04/03/12<br>REV | QUALITY SYMBOLS<br>MAJOR<br>▼ = 0<br>CRITICAL<br>△ = 0 | GENERAL TOLERANCES:<br>(UNLESS SPECIFIED) | SCALE<br>4 : 1 | DESIGN UNITS<br>mm <input checked="" type="checkbox"/> INCH <input type="checkbox"/> | DRAWN BY & DATE<br>MOSULLIV 99/02/12 | DIMENSIONS: <input type="checkbox"/> INCH <input type="checkbox"/> mm <input type="checkbox"/> INCH <input type="checkbox"/> mm <input type="checkbox"/> ONLY |  | SHT                | REV                 |
|-------------|----------------|-----------|-----------|---|--|---|----------------|--|--------------------------------------|---|--|--------------------|---------------------|
|             |                |           |           |   |  |   |                |  |                                      | TITLE: DVI - INTERGRATED<br>RIGHT ANGLE<br>RECEPTACLE CONNECTOR   |  |                    |                     |
| 74320-1000  | 1              | .092/2.34 | .080±.020 |   |  |   |                |  |                                      | APPROVED BY & DATE<br>NELLIGAN 99/02/15   |  | MOLEX INCORPORATED |                     |
| 74320-1001  | 1              | .050/1.27 | .203±0.51 |   |  |   |                |  |                                      | CAD FILENAME<br>SD7432001.S01   |  | MATERIAL NO.       | DRAWING NO.         |
| 74320-1003  | 1              | .123/3.12 | .110/2.79 |   |  |   |                |  |                                      | SEE CHART   |  | SD-74320-001       | SHEET NO.<br>1 OF 2 |
| 74320-1004  | 2              | .092/2.34 | .080±.020 |   |  |   |                |  |                                      | THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX<br>INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION.                             |  |                    | SIZE<br>C           |
| 74320-1005  | 2              | .050/1.27 | .203±0.51 |   |  |   |                |  |                                      |   |  |                    |                     |
| 74320-1006  | 2              | .110/2.79 | .110/2.79 |   |  |   |                |  |                                      |   |  |                    |                     |
| 74320-1007  | 2              | .123/3.12 | .110/2.79 |   |  |   |                |  |                                      |   |  |                    |                     |



## PLATING OPTIONS:

## PLATING OPTION #1:

OUTER SHIELD -- 150 $\mu$ "/(3.75um) MIN. BRIGHT TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVER COPPER FLASH.  
 .075"/(1.91mm) CONTACTS -- 30 $\mu$ "/(0.75um) MIN. SELECT GOLD AND 100 $\mu$ "/(2.50um) MIN. SELECT PURE TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.  
 .100"/(2.54mm) CONTACTS -- 30 $\mu$ "/(0.75um) MIN. SELECT GOLD AND 100 $\mu$ "/(2.50um) MIN. SELECT PURE TIN. OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.

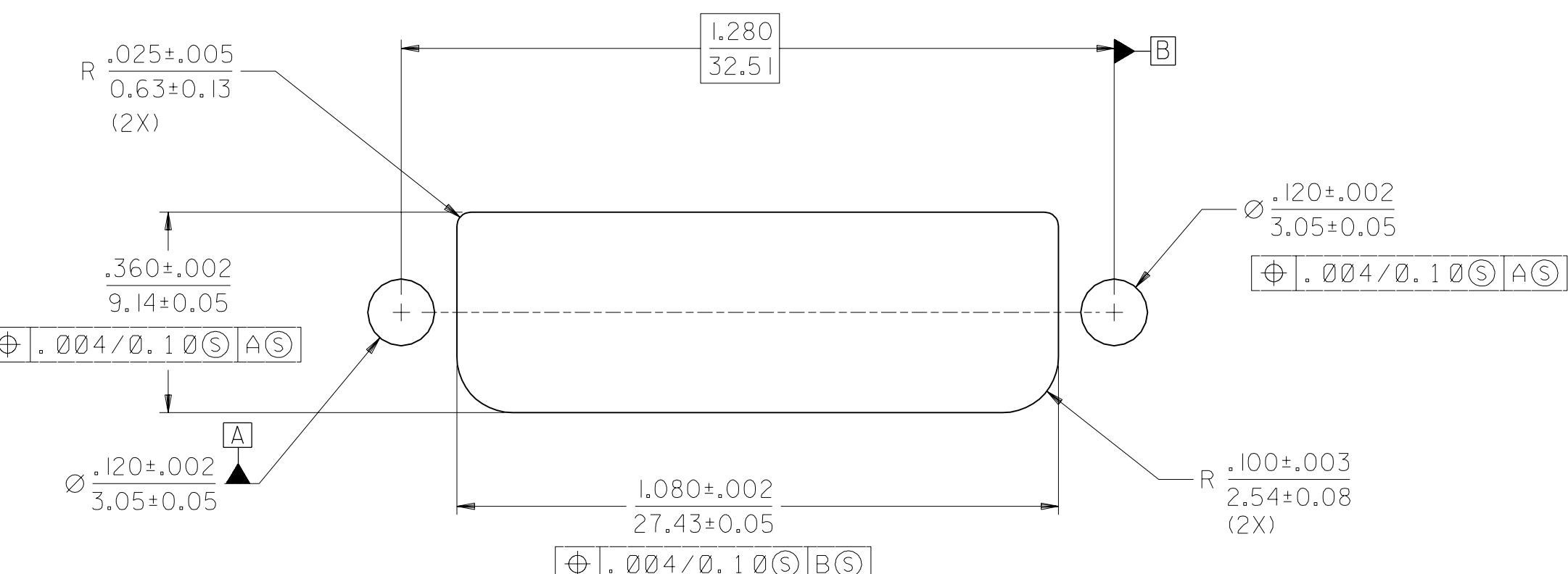
MICROCROSS GROUND -- 30 $\mu$ "/(0.75um) MIN. SELECT GOLD AND 100 $\mu$ "/(2.50um) MIN. SELECT PURE TIN. OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.

## PLATING OPTION #2:

OUTER SHIELD -- 150 $\mu$ "/(3.75um) MIN. BRIGHT TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVER COPPER FLASH.  
 .075"/(1.91mm) CONTACTS -- SELECT GOLD FLASH AND 100 $\mu$ "/(2.50um) MIN. SELECT PURE TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.

.100"/(2.54mm) CONTACTS -- SELECT GOLD FLASH AND 100 $\mu$ "/(2.50um) SELECT PURE TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.

MICROCROSS GROUND -- 30 $\mu$ "/(0.75um) MIN. SELECT GOLD AND 100 $\mu$ "/(2.50um) MIN. SELECT PURE TIN OVER 50 $\mu$ "/(1.27um) MIN. NICKEL OVERALL.



RECOMMENDED PANEL CUT-OUT

PANEL THICKNESS: .030" - .040"

| MODIFICATION<br>EC NO. SH2004-0130<br>DRWN: DXUE 04/03/05<br>CHR: HARVEY 04/03/12<br>APPR: SHUJIANG 04/03/12 |   | QUALITY SYMBOLS<br>MAJOR<br>▼ = 0<br>CRITICAL<br>△ = 0 | GENERAL TOLERANCES:<br>(UNLESS SPECIFIED) |      | SCALE<br>4 : 1<br>mm X INCH | DESIGN UNITS<br>mm X INCH | DRAWN BY & DATE<br>MOSULLIV 99/02/12 | CHECKED BY & DATE<br>MOSULLIV 99/02/12 | APPROVED BY & DATE<br>NELLIGAN 99/02/15 | CAD FILENAME<br>SD7432001.S02 | MATERIAL NO.<br>SEE CHART | DRAWING NO.<br>SD-74320-001 | SHEET NO.<br>2 | REVISE ON<br>CAD ONLY |
|--|---|--|---|------|-----------------------------|---------------------------|--------------------------------------|--|---|-------------------------------|---------------------------|-----------------------------|----------------|-----------------------|
| REV  | DESCRIPTION   |  | mm  | INCH |                             |                           |                                      |  |   |                               |                           |                             |                |                       |
| E  | ANGULAR: $\pm 1/2^\circ$  |  |   |      |                             |                           |                                      |  |   |                               |                           |                             |                |                       |
|  | DRAFT WHERE APPLICABLE MUST<br>REMAIN WITHIN DIMENSIONS   |  |   |      |                             |                           |                                      |  |   |                               |                           |                             |                |                       |
|  | THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX<br>INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION. |  |   |      |                             |                           |                                      |  |   |                               |                           |                             |                |                       |