

SMW70N06-14 Transistors

N-Channel Enhancement MOSFET

Military/High-Rel N

$V_{(BR)DSS}$ (V) 60

$V_{(BR)GSS}$ (V) 20

$I_{(D)}$ Max. (A) 70#

$I_{(DM)}$ Max. (A) Pulsed $I_{(D)}$ 48#

@Temp (°C) 100

I_{DM} Max (@25°C Amb) 280#

@Pulse Width (s) (Condition)

Absolute Max. Power Diss. (W) 150#

Minimum Operating Temp (°C) -55

Maximum Operating Temp (°C) 150

Thermal Resistance Junc-Case 0.83

Thermal Resistance Junc-Amb. 40

$V_{(GS)th}$ Max. (V) 4.0

$V_{(GS)th}$ (V) (Min) 2.0

@ $V_{(DS)}$ (V) (Test Condition)

@ $I_{(D)}$ (A) (Test Condition) 1m

$I_{(DSS)}$ Max. (A) 25u

@ $V_{(DS)}$ (V) (Test Condition) 48

@Temp (°C) (Test Condition) 25

$I_{(GSS)}$ Max. (A) 500n