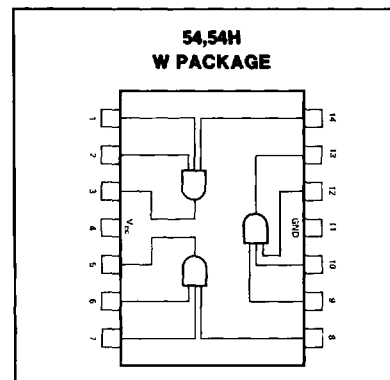
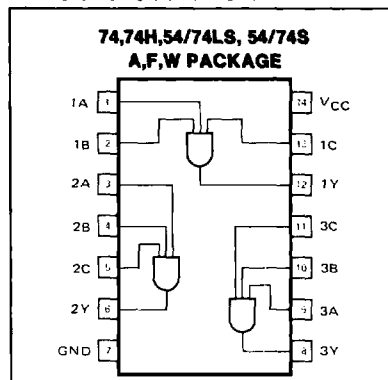


SPEED/PACKAGE AVAILABILITY

54 F,W	74 A,F
54H F,W	74H A,F
54LS F,W	74LS A,F
54S F,W	74S A,F

PIN CONFIGURATION



SWITCHING CHARACTERISTICS $V_{CC} = 5V$, $T_A = 25^\circ C$

TEST CONDITIONS	54/74 $C_L = 15pF$ $R_L = 400\Omega$			54/74H $C_L = 25pF$ $R_L = 280\Omega$			54/74LS $C_L = 15pF$ $R_L = 2k\Omega$			54/74S $C_L = 15pF$ $R_L = 280\Omega$			UNIT
	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
Propagation delay time t_{PLH} Low-to-high		17.5	27		7.6	12		9	15	2.5	4.5 $C_L = 50pF$ 6	7	ns
t_{PHL} High-to-low		12	19		8.8	12		9	20	2.5	5 $C_L = 50pF$ 7.5	7.5	ns

Load circuit and typical waveforms are shown at the front of section.

10101

TRIPLE 3-INPUT NAND GATE W/OPEN COLLECTOR OUTPUTS

SPEED/PACKAGE AVAILABILITY

54LS F,W	74LS A,F
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SWITCHING CHARACTERISTICS $V_{CC} = 5V$, $T_A = 25^\circ C$

PARAMETER	TEST CONDITIONS	LIMITS			
		MIN	TYP	MAX	UNIT
t_{PLH} Propagation delay time, low-to-high-level output	$C_L = 15pF$, $R_L = 2k\Omega$		17	32	ns
t_{PHL} Propagation delay time, high-to-low-level output			15	28	ns

Load circuit and typical waveforms are shown at the front of section.

PIN CONFIGURATION

